

LTC1384

5V Low Power RS232 Transceiver with 2 Receivers Active in Shutdown

### FEATURES

- Operates from a Single 5V Supply Low Supply Current: I<sub>CC</sub> = 220µA
- I<sub>CC</sub> = 35µA in Shutdown Mode with Both Receivers Kept Alive
- ESD Protection Over ±10kV
- Uses Small Capacitors: 0.1µF
- Operates to 120kBaud
- Output Overvoltage Does Not Force Current Back into Supplies
- RS232 I/O Lines Can Be Forced to ±25V Without Damage
- Pin Compatible with LT1180A

# **APPLICATIONS**

- Notebook Computers
- Palmtop Computers

# DESCRIPTION

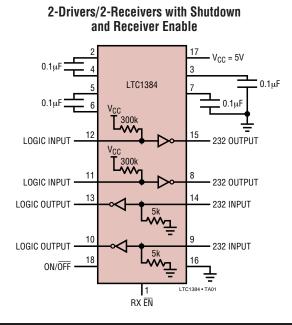
The LTC<sup>®</sup>1384 is an ultra-low power 2-driver/2-receiver RS232 transceiver that operates from a single 5V supply. The charge pump requires only four space-saving  $0.1\mu$ F capacitors.

The transceiver operates in one of two modes, Normal and Shutdown. In the Normal mode,  $I_{CC}$  is only  $220\mu A$  with the driver outputs unloaded. In the Shutdown mode, the charge pump is turned off, the driver outputs are forced into three-state, both receivers are kept active and  $I_{CC}$  drops to  $35\mu A$ . The receiver outputs may be forced into three-state at any time using the receiver enable (RX  $\overline{\text{EN}}$ ) pin.

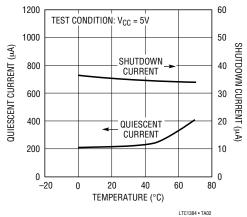
The LTC1384 is fully compliant with all data rate and overvoltage RS232 specifications. The transceiver can operate up to 120kbaud with a 2500pF,  $3k\Omega$  load. Both driver outputs and receiver inputs can be forced to  $\pm 25V$  without damage and can survive multiple  $\pm 10kV$  ESD strikes.

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### TYPICAL APPLICATION



# Quiescent and Shutdown Supply Current vs Temperature



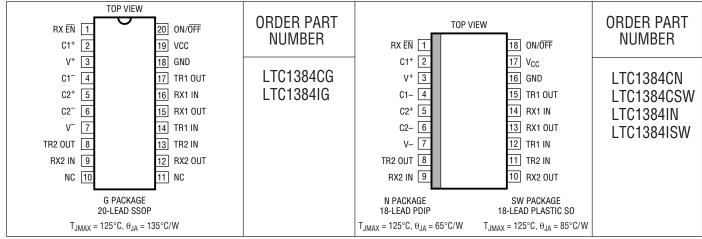
**TECHNOLOGY** 

# **ABSOLUTE MAXIMUM RATINGS**

Supply Voltage (V <sub>CC</sub> )
Input Voltage
Driver $-0.3V$ to V <sub>CC</sub> + 0.3V
Receiver25V to 25V
Digital Input $-0.3V$ to V <sub>CC</sub> + 0.3V
Output Voltage
Driver
Receiver – 0.3V to $V_{CC}$ + 0.3V

Short-Circuit Duration	
V <sup>+</sup>	30 sec
V <sup>-</sup>	30 sec
Driver Output	Indefinite
Receiver Output	Indefinite
Operating Temperature Range	
LTC1384C	0°C to 70°C
LTC1384I	– 40°C to 85°C
Storage Temperature Range	. –65°C to 150°C
Lead Temperature (Soldering, 10 sec).	300°C

# PACKAGE/ORDER INFORMATION



Consult LTC Marketing for parts specified with wider operating temperature ranges.

# DC ELECTRICAL CHARACTERISTICS The • denotes specifications which apply over the full operating

temperature range.  $V_{CC} = 5V$ ,  $C1 = C2 = C3 = C4 = 0.1 \mu F$ ,  $V_{ON/\overline{OFF}} = V_{CC}$ , RX EN = 0V, unless otherwise noted.

PARAMETER	CONDITIONS			MIN	ТҮР	MAX	UNITS
Any Driver							
Output Voltage Swing	3k to GND	Positive Negative	•	5.0 -5.0	7.0 -6.5		V V
Logic Input Voltage Level	Input Low Level (V <sub>OUT</sub> = High) Input High Level (V <sub>OUT</sub> = Low)		•	2.0	1.4 1.4	0.8	V V
Logic Input Current	$V_{IN} = V_{CC}$ $V_{IN} = 0V$		•		-20	5 -40	μΑ μΑ
Output Short-Circuit Current	$V_{OUT} = 0V$			±9	±12		mA
Output Leakage Current	Shutdown or V <sub>CC</sub> = 0V (Note 3), V <sub>OU</sub>	T = ±10V	•		±10	±500	μA
Any Receiver			·				
Input Voltage Thresholds	Input Low Threshold Input High Threshold		•	0.8	1.3 1.7	2.4	V V
Hysteresis			•	0.1	0.4	1	V
Input Resistance	$-10V \le V_{IN} \le 10V$			3	5	7	kΩ
Output Voltage	Output Low, $I_{OUT} = -1.6mA$ (V <sub>CC</sub> = 5 Output High, $I_{OUT} = 160\mu A$ (V <sub>CC</sub> = 5		•	3.0	0.2 3.2	0.4	V V
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**DC ELECTRICAL CHARACTERISTICS** The • denotes specifications which apply over the full operating temperature range.  $V_{CC} = 5V$ ,  $C1 = C2 = C3 = C4 = 0.1 \mu F$ ,  $V_{ON/\overline{OFF}} = V_{CC}$ , RX EN = 0V, unless otherwise noted.

PARAMETER	CONDITIONS		MIN	ТҮР	MAX	UNITS
Output Short-Circuit Current	Sinking Current, V <sub>OUT</sub> = V <sub>CC</sub>		-15	-40		mA
	Sourcing Current, V <sub>OUT</sub> = 0V		10	20		mA
Output Leakage Current	$\overline{EN} = V_{CC}, 0V \le V_{OUT} \le V_{CC}$	•		1	10	μA
Power Supply Generator		L. L.				
V <sup>+</sup> Output Voltage	I <sub>OUT</sub> = 0mA			8.0		V
	I <sub>OUT</sub> = 8mA			7.5		V
V <sup>-</sup> Output Voltage	I <sub>OUT</sub> = 0mA			-8.0		V
	$I_{OUT} = -8mA$			-7.0		V
Supply Rise Time	Shutdown to Turn-On			0.2		ms
Power Supply		·				
V <sub>CC</sub> Supply Current	No Load (Note 2), 0°C to 70°C	•		0.22	0.5	mA
	No Load (Note 2), -40°C to 85°C	•		0.35	1.0	mA
Supply Leakage Current (V <sub>CC</sub> )	Shutdown (Note 3)	•		35	50	μA
Digital Input Threshold Low		•		1.4	0.8	V
Digital Input Threshold High		•	2.0	1.4		V

#### **AC CHARACTERISTICS** The • denotes specifications which apply over the full operating temperature range. $V_{CC} = 5V$ , $C1 = C2 = C3 = C4 = 0.1 \mu$ F, unless otherwise noted.

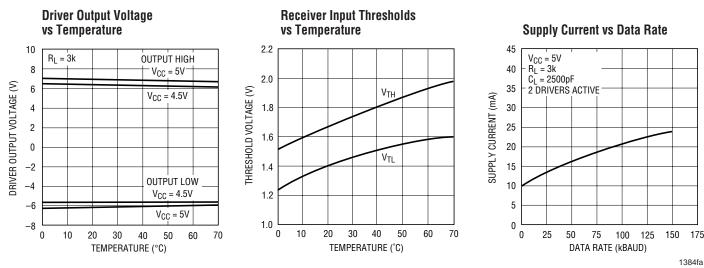
PARAMETER	CONDITIONS		MIN	ТҮР	MAX	UNITS
Slew Rate	R <sub>L</sub> = 3k, C <sub>L</sub> = 51pF			8	30	V/µs
	$R_{L} = 3k, C_{L} = 2500pF$		3	5		V/µs
Driver Propagation Delay	t <sub>HLD</sub> (Figure 1)	•		2	3.5	μS
(TTL to RS232)	t <sub>LHD</sub> (Figure 1)	•		2	3.5	μS
Receiver Propagation Delay	t <sub>HLR</sub> (Figure 2)	•		0.3	0.8	μS
(RS232 to TTL)	t <sub>LHR</sub> (Figure 2)	•		0.3	0.8	μS

Note 1: Absolute Maximum Ratings are those values beyond which the life of the device may be impaired.

Note 3: Measurements made in the Shutdown mode are performed with  $V_{ON/OFF} = 0V$ .

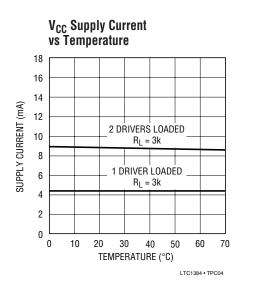
Note 2: Supply current is measured with driver and receiver outputs unloaded.

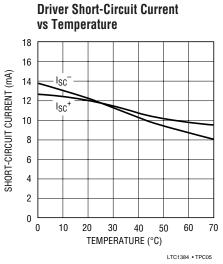
# **TYPICAL PERFORMANCE CHARACTERISTICS**



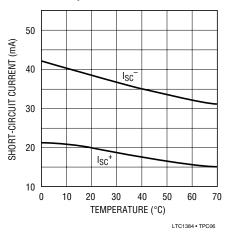


# **TYPICAL PERFORMANCE CHARACTERISTICS**

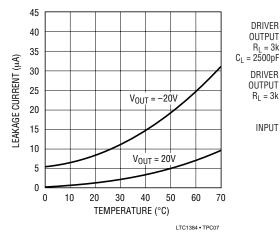




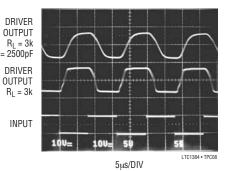
# Receiver Short-Circuit Current vs Temperature



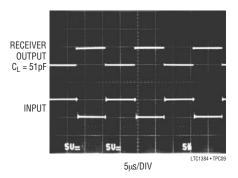
Driver Leakage in Shutdown vs Temperature



#### **Driver Output Waveforms**



#### **Receiver Output Waveforms**





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# PIN FUNCTIONS

 $\bm{V_{CC}}$ : 5V Input Supply Pin. This pin should be decoupled with a  $0.1\mu F$  ceramic capacitor.

GND: Ground Pin.

**ON/OFF:** TTL/CMOS Compatible Shutdown Pin. A logic low puts the device in the Shutdown mode independent of the RX  $\overline{\text{EN}}$  pin. The supply current of the device drops to  $35\mu\text{A}$  (two receivers alive) and both driver outputs are forced into three-state.

**RX EN:** TTL/CMOS Compatible Receiver Enable Pin. A logic high forces the receiver outputs into three-state. A logic low enables the receiver outputs.

**V**<sup>+</sup>: Positive Supply Output (RS232 Drivers). V<sup>+</sup>  $\cong$  2V<sub>CC</sub> – 2V. This pin requires an external capacitor C = 0.1µF for charge storage. The capacitor may be tied to ground or V<sub>CC</sub>. With multiple devices, the V<sup>+</sup> and V<sup>-</sup> pins may share a common capacitor. For large numbers of devices, increasing the size of the shared common storage capacitors is recommended to reduce ripple.

**V**<sup>-</sup>: Negative Supply Output (RS232 Drivers). V<sup>-</sup> $\approx$ -(2V<sub>CC</sub> - 2V). This pin requires an external capacitor C = 0.1  $\mu$ F for charge storage.

**C1<sup>+</sup>, C1<sup>-</sup>, C2<sup>+</sup>, C2<sup>-</sup>:** Commutating Capacitor Inputs. These pins require two external capacitors  $C = 0.1 \mu$ F: one from C1<sup>+</sup> to C1<sup>-</sup> and another from C2<sup>+</sup> to C2<sup>-</sup>. To maintain charge pump efficiency, the capacitor's effective series resistance should be less than 2 $\Omega$ .

**TR IN:** RS232 Driver Input Pins. Inputs are TTL/CMOS compatible. The inputs of unused drivers can be left unconnected since 300k input pull-up resistors to  $V_{CC}$  are included on chip. To minimize power consumption, the internal driver pull-up resistors are disconnected from  $V_{CC}$  in the Shutdown mode.

**TR OUT:** Driver Outputs at RS232 Voltage Levels. Outputs are in a high impedance state when in the Shutdown or  $V_{CC}$  = 0V. The driver outputs are protected against ESD to ±10kV for human body model discharges.

**RX IN:** Receiver Inputs. These pins can be forced to  $\pm 25V$  without damage. The receiver inputs are protected against ESD to  $\pm 10kV$  for human body model discharges. Each receiver provides 0.4V of hysteresis for noise immunity.

**RX OUT:** Receiver Outputs with TTL/CMOS Voltage Levels. A logic high at RX EN puts the outputs into three-state.

# SWITCHING TIME WAVEFORMS

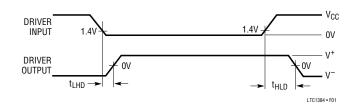


Figure 1. Driver Propagation Delay Timing

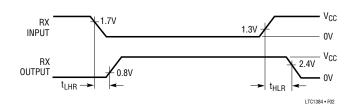
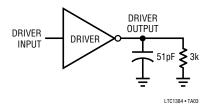


Figure 2. Receiver Propagation Delay Timing

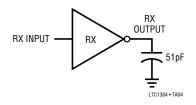


# **TEST CIRCUITS**

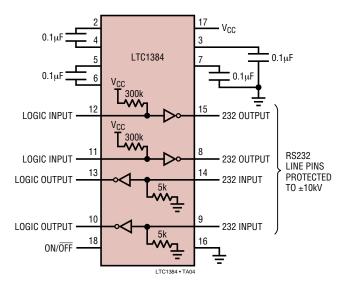
#### **Driver Timing Test Load**



**Receiver Timing Test Load** 

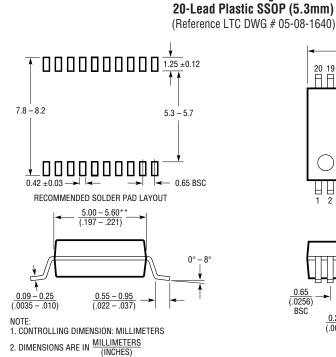


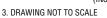




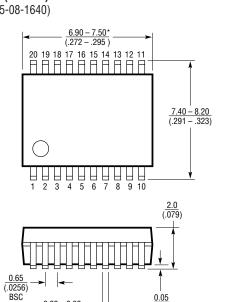


#### PACKAGE DESCRIPTION





- \*DIMENSIONS DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .152mm (.006") PER SIDE
- \*\*DIMENSIONS DO NOT INCLUDE INTERLEAD FLASH. INTERLEAD
- FLASH SHALL NOT EXCEED .254mm (.010") PER SIDE



0.22 - 0.38

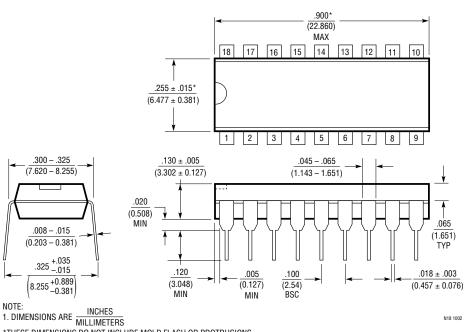
 $\frac{0.22}{(.009 - .015)}$ 

 $\frac{0.05}{(.002)}$ 

G20 SSOP 08

N Package 18-Lead PDIP (Narrow .300 Inch) (Reference LTC DWG # 05-08-1510)

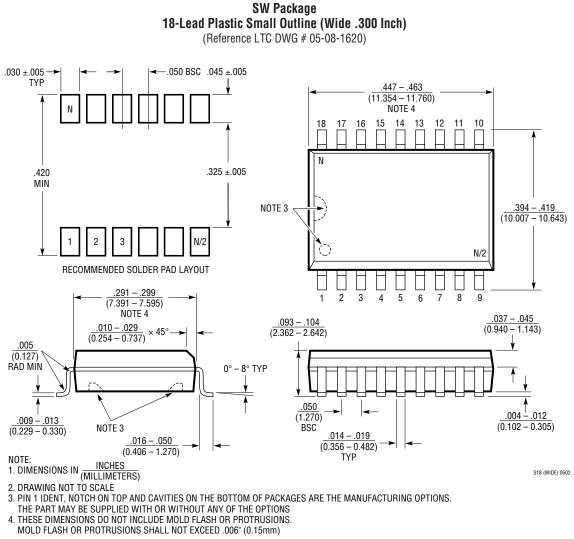
**G** Package



\*THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS. MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED .010 INCH (0.254mm)

Information furnished by Linear Technology Corporation is believed to be accurate and reliable. However, no responsibility is assumed for its use. Linear Technology Corporation makes no representation that the interconnection of its circuits as described herein will not infringe on existing patent rights.

### PACKAGE DESCRIPTION



### **RELATED PARTS**

PART NUMBER	DESCRIPTION	COMMENTS
LT1780/LT1781	5V, 2 Driver, 2 Receiver RS232 Transeivers	±15kV ESD per IEC 1000-4
LTC1382	5V, 2 Driver, 2 Receiver RS232 Transceiver	220µA Supply Current, 0.2µA in Shutdown
LTC1383	5V, 2 Driver, 2 Receiver RS232 Transceiver	220µA Supply Current, Narrow 16-pin SO
LTC1385	3.3V, 2 Driver, 2 Receiver RS562 Transceiver	220µA Supply Current, 2 Receivers Active in Shutdown
LTC1386	3.3V, 2 Driver, 2 Receiver RS562 Transceiver	220µA Supply Current, Narrow 16-pin SO

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